Assessment and subject description

Obuda University								
Kandó Kálmán Faculty of Electrical Engineering Institute of Technology					Institute of Technology	Microelectronics and		
Subject name and	d code: Electr	onic Techn	ology KEXE	T1ABNE		Credits: 4		
Full-time, Spring Semester								
Course: Electrical I	Course: Electrical Engineering							
Responsible: C	Csikósné Dr Pap	Andrea	Teaching	staff:	Gröller Gyö	rgy		
Prerequisites:	KEXVR	1ABNE						
Contact hours	Lecture: 2	Class disc	cussion: 0 Lab hours: 2 Tutorial: 0					
per week:								
Assessment and	exam	exam						
evaluation:								
		Subj	ect descript	tion				
Aims: Review mat	erials and proce	esses used	in electroni	c indust	ry. Construct	ions of micro	pelectronic	
parts and devic	es and their	manufact	turing met	hods.	Basic techn	ologies of	electronic	
interconnections.	Microelectronic	s is one of	the main f	ield of h	itech. To un	derstand the	e advanced	
products is necess	ary to know the	ir technolo	gical backg	round. D	evelop labora	atorial skills	in the field	
of PCB technology	/.							
Topics to be cover	ed:						T	
		Topics				Week	Lessons	
Introduction to the	e technology ar	nd electron	ic industry	Discrete	parts,	1	2	
substrates, integrated circuits, modules and devices								
Manufacturing of Printed Wiring Boards: patterning; steps of lithography,						2	2	
screen printing, etching, electroless and galvanic plating.						-		
Single and double side PCB; main steps of production. Multilayer PCB-s,						3	2	
coo-laminated and sequential methods.								
High Density Interconnections (HDI); new requirements, new processes.							_	
Control methods.						4	2	
Design for Manufacturing (DfM).								
Manufacturing of	the electronic r	nodules; Si	urface Mou	nted Teo	chnology			
(SMT)						5	2	
Soldering basics. Solder paste printing, shooting of devices, reflow								
Soluering.	SMT II: wave coldering inspection methods, rework, ESD protection					2		
SWITH: wave soldering, inspection methods, rework. ESD protection.					0	2		
						/	2	
Hybrid Integrated Circuits (HIC)					8	2		
Thin Film HIC: Vacu	um deposition	methods.						
Thick Film HIC: scr	een printing me	thods	ь					
Thin and thick pas	hin and thick passive circuits, trimming methods					9	2	
Multicnip Modules	Aultichip Modules: types, manufacturing methods							
noliday						10	2	
Introduction to the semiconductor technology: Materials (silicon and								
compounds semic	onductors)					11	2	
Main processes of	Main processes of IC technology: lithography, doping, oxidizing, etching,							
epitaxy and vacuu	m deposition m	ethods						
Micro Electro-Mechanical Systems (MEMS)						2		
Student presentat	ions						_	

Printed electronics: materials and tec	13	2					
Student presentations							
Laboratory	1	2					
Monufacturing Double side through	hala plated DCD drilling making	1	3				
hole conductive	2	3					
Photolithography, galvanic plating	3	3					
Solder mask preparation and patternin	4	3					
Assembly processes, soldering TH and	5	3					
Design: Circuit diagram I, borders, fir Block processes,	6	3					
Circuit diagram II Drawing a schemat footprint, wiring, block operations. Bo	7	3					
Routing, placing components. Auto ro	8	3					
Design Rule Check (DRC), practising	9	3					
A Requirements of the signature: The final mark contributors: Type of exam: Evaluation of the exam test:	ssessment and evaluationParticipation in the lectures and la Perform laboratory tasks and laboratory tasks and laboratory tasks and laboratory is Laboratory works: a 33%Laboratory works:33%Exam test:66%Written exam $0 - 49 \%$ 1 $50 - 59 \%$ 2 $60 - 69 \%$ 3 $70 - 84 \%$ 4	ab practices is co reports 5 better than 40%	mpulsory				
85 – 100% 5 Suggested material Gröller György: Electronic technology (presentations and handouts) in Moodle or							
http://www.uni-obuda.hu/users/grollerg/Electronic-technology/ Recommended: Happy Holden: The HDI Handbook <u>http://www.hdihandbook.com/download.php</u>							
Comment:							