Assessment and subject description

Óbuda University										
, 5 5					Institute of Microelectronics and Technology					
Subject name and	Credits: 3									
Subject name and code:Manufacturing Engineering I. KEXGTBABNECredits: 3Full-time, Spring Semester										
Course: Mechatronical Engineering BSc										
Responsible: Dr Bugyjás József Teaching staff: Gröller Gyö										
Prerequisites:										
Contact hours per week:	Lecture: 2	Class discu	ussion: 0	Lab hours: 1		Tutorial: 0				
Assessment and	lé									
evaluation:										
		Subj	ect descript	tion						
Aims: The first	part of this su	ıbject is a	bout the	technol	ogies of the	e electronics	industry.			
Microelectronics i	s one of the mo	st importar	nt field of hi	tech. To	o understand	the advance	d products			
is necessary to know	ow their technol	logical back	ground.							
Topics to be cover	ed:									
		Topics				Week	Lessons			
Introduction to th	e technology an	d electron	ic industry.	Short h	nistory.					
Hierarchy of the products; discrete parts, integrated circuits, modules and						1	2			
devices.										
Electronic interconnection technology. PCB basics: photolithography,						2	2			
screen printing, etching, electroless and galvanic plating.						2	2			
Single and double side PCB; main steps of production. Multilayer PCB-s,							2			
High Density Interconnections (HDI); new requirements, new processes.										
Control methods.							2			
Design for Manufacturing (DfM).										
Manufacturing of the electronic modules; Surface Mounted Technology										
(SMT)						5	2			
Soldering basics. Solder paste printing, device shooting, reflow soldering.										
SMT II: wave soldering, inspection methods, rework. ESD protection.						6	2			
Test 1						7	2			
Hybrid Integrated Circuits (HIC)						8	2			
Thin Film HIC: vacu	-									
	c screen printing methods					0	2			
	in and thick passive circuits, trimming of resistors <i>ultichip Modules:</i> types, manufacturing methods				9	2				
· · · ·	s: types, manufa	10	2							
holiday	the semiconductor technology: Materials (silicon and						2			
		r technolog	gy: Materia		on and					
compounds semiconductors)					11	2				
Main processes of IC technology: lithography, doping, oxidizing, etching, epitaxy and vacuum deposition methods						11	2			
Student presentat		ethous								
-	ro-Mechanical Systems (MEMS)									
Student presentat						12	2			
Organic and printed electronics: materials and technology						13	2			
Student presentations						1.4	2			
Test 2						14	2			

Laboratory practise	es						
Introduction, safety	6	3					
PCB manufacturing	7	3					
Photolithography	8	3					
Screen printing	9	3					
Component placing, reflow and hand soldering 10							
		Assessment and evaluation					
Requirements:	Participatior	Participation in the lectures and lab practices is compulsory.					
	Tests about	Tests about theory are 25 points each					
	n worth 20 points						
Laboratory work, lab report and the test worth 30 points.							
Evaluation of the m	0 – 49 points 1						
		50 – 59 points 2					
		60 – 69 points 3					
		70 – 79 points 4					
		80 – 100 points 5					
		Suggested material					
Gröller György: Ele	ctronic technolog	y (presentations and handouts) Moodle					
		/grollerg/Manufacturing_engineering/					
		HDI Handbook <u>http://www.hdihandbook</u>		oad.php			
Comment:							